

Search Notes

Application/Control No.

10/724,011

Examiner

Peter Coughlan

Applicant(s)/Patent under
Reexamination

HEER, CHRISTOPH

Art Unit

2129

SEARCHED

Class	Subclass	Date	Examiner
340	10.1	3/26/2007	PDC
340	10.3	3/26/2007	PDC
712	11	3/26/2007	PDC
706	16	3/26/2007	PDC
706	22	3/26/2007	PDC
706	33	3/26/2007	PDC
706	43	3/26/2007	PDC

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
East--d flip flops, node, input output, if then else, LUT, comparison, MUX multiplexer, CLB, configurable logic blocks	3/26/2007	PDC
East--II--look up table, logic control, register, input control node, schematic, switch, bus, comparator, event detector,	3/26/2007	PDC
East--III--xilinx, infineon,	3/26/2007	PDC
IEEE--Christoph Heer, circuit 'if then else' d flip flop, LUT look up table, CLB configurable logic block, , schematic	3/26/2007	PDC
Dogpile--Christoph Heer, circuit 'if then else' d flip flop, LUT look up table, CLB configurable logic block, , schematic	3/26/2007	PDC
Inventor Christoph Heer	3/26/2007	PDC